EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	996	((integrated adj circuit) or (printed adj circiut)) with (trace with pad)	US-PGPUB; USPAT; JPO; DERWENT	OR -	ON	2006/06/09 08:33
L3	1692	(printed adj circuit) with (trace with pad\$1)	US-PGPUB; USPAT; JPO; DERWENT	OR	ON	2006/06/09 08:34
L4	6027	circuit with substrate with trace	US-PGPUB; USPAT; JPO; DERWENT	OR	ON	2006/06/09 08:34
L5	62	2 and 3 and 4	US-PGPUB; USPAT; JPO; DERWENT	OR	ON	2006/06/09 08:34
L6	28	He-Tom-Xiaohai.in. or Haller-Matthew-Iiin. or Parramon-Jordi.in. or Marnfeldt-Goran-N.in.	US-PGPUB; USPAT; JPO; DERWENT	OR	ON	2006/06/09 08:35
L7	0	5 and 6	US-PGPUB; USPAT; JPO; DERWENT	OR	ON	2006/06/09 08:35
L8	1	2 and 6	US-PGPUB; USPAT; JPO; DERWENT	OR	ON	2006/06/09 08:35
L9	0	3 and 6	US-PGPUB; USPAT; JPO; DERWENT	OR	ON	2006/06/09 08:35
L10	1	4 and 6	US-PGPUB; USPAT; JPO; DERWENT	OR	ÓN	2006/06/09 08:36
L11	1336	redistribut\$3 near (surface or layer)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/06/09 08:36
L12	10	_conduct\$3_near_redistribut\$3_near material	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/06/09 08:37
L13	1	6 and 11	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/06/09 08:37

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L14	4	12 and 11	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/06/09 08:37
L15	4	11 and 14		OR	ON	2006/06/09 08:37
			USPAT; EPO; JPO			

6/9/2006 8:37:42 AM